

# G.729A-C64x Data Sheet

## Introduction

Beijing YiYuanZhuCheng (YYZC) G.729A voice coding software is an implementation of ITU Recommendation G.729 8 kbit/s CS-ACELP Speech Codec with Annex A. The software runs on the Texas Instruments (TI) TMS320C64X family of Digital Signal Processors. The G.729A voice codec provides near toll speech quality at a compressed data rate of 8 kbit/s. The G.729A is used in wireless voice, voice-over-packet-networks, multimedia, and voice circuit multiplexing applications. Both encoder and decoder pass all ITU-T test vectors.

## Resource Requirements<sup>\*†‡</sup>

Software	MCPS (Peak)	MCPS (Average)
Encoder	2.51	2.44
Decoder	0.57	0.54

Software	Program Memory	Const Data Memory	Scratch Memory	Stack Memory	Per Channel Data Memory
Encoder	30	5.5	1.9	1.3	1.7
Decoder	12	4.8	0.2	0.6	1.8
Encoder & Decoder	35	5.6	1.9	1.3	3.5

## Money Back Guarantee

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## Contact

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\* All MCPS results are based on TI CCS2.20.18 C64xx Cycle Accurate Simulator when encoding/decoding the ITU-T test vectors. The real cycles may vary when running on different C64x DSPs and different inputs.

† All memory usage figures are given in unit of Kbyte.

‡ YYZC keeps the right to update the software without updating these figures.