

G.729A-C62x Data Sheet

Introduction

Beijing YiYuanZhuCheng (YYZC) G.729A voice coding software is an implementation of ITU Recommendation G.729 8 kbit/s CS-ACELP Speech Codec with Annex A. The software runs on the Texas Instruments (TI) TMS320C62X family of Digital Signal Processors. The G.729A voice codec provides near toll speech quality at a compressed data rate of 8 kbit/s. The G.729A is used in wireless voice, voice-over-packet-networks, multimedia, and voice circuit multiplexing applications. Both encoder and decoder pass all ITU-T test vectors.

Resource Requirements^{*†‡}

| Software | MCPS (Peak) | MCPS (Average) |
|----------|-------------|----------------|
| Encoder | 3.42 | 3.33 |
| Decoder | 0.65 | 0.61 |

| Software | Program Memory | Const Data Memory | Scratch Memory | Stack Memory | Per Channel Data Memory |
|-------------------|----------------|-------------------|----------------|--------------|-------------------------|
| Encoder | 34 | 5.5 | 1.9 | 1.3 | 1.7 |
| Decoder | 15 | 4.8 | 0.2 | 0.6 | 1.8 |
| Encoder & Decoder | 39 | 5.6 | 1.9 | 1.3 | 3.5 |

Money Back Guarantee

We will return 50% license fees to our customers who find a better solution with 5% lower MIPS within one year of purchase. No other company in the world can guarantee this except us.

Contact

Web: www.yydsp.com

Email: yyzdsp@gmail.com

* All MCPS results are based on TI CCS2.20.18 C62xx Cycle Accurate Simulator when encoding/decoding the ITU-T test vectors. The real cycles may vary when running on different C62x DSPs and different inputs.

† All memory usage figures are given in unit of Kbyte.

‡ YYZC keeps the right to update the software without updating these figures.