

## G.726-C64x Data Sheet

### Introduction

Beijing YiYuanZhuCheng (YYZC) G.726 voice coding software is an implementation of ITU Recommendation G.726 40, 32, 24, 16 kbit/s Adaptive Differential Pulse Code Modulation (ADPCM). The software runs on the Texas Instruments (TI) TMS320C64X family of Digital Signal Processors. Both encoder and decoder pass all ITU-T test vectors when reset function is called in the beginning. But the homing test is failed, since the original ITU C source code fails too.

### Resource Requirements<sup>\*†‡</sup>

Software	MCPS (Peak)	Algorithm Delay
Encoder	0.95	1 sample (0.125 ms)
Decoder	1.15	1 sample (0.125 ms)

Software	Program Memory	Const Data Memory	Scratch Memory	Stack Memory	Per Channel Data Memory
Encoder	3.1	0.3	0	0	0.05
Decoder	3.1	0.3	0	0	0.05
Encoder & Decoder	3.1	0.3	0	0	0.05

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### Contact

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\* All MCPS results are based on the worst case for any input and any bitrate. Only CPU cycles are counted.

† All memory usage figures are given in units of Kbyte.

‡ YYZC keeps the right to update the software without updating these figures.