

G.723.1-C62x Data Sheet

Introduction

Beijing YiYuanZhuCheng Technologies (YYZC) G.723.1 voice coding software is an implementation of ITU Recommendation G.723.1 6.3 & 5.3 kbit/s CELP Speech Codec with Annex A. The software runs on the Texas Instruments (TI) TMS320C62X family of Digital Signal Processors. The G.723.1 voice codec provides near toll speech quality at a compressed data rate of 6.3 or 5.3 kbit/s. The G.723.1 is used in wireless voice, voice-over-packet-networks, multimedia, and voice circuit multiplexing applications. Both encoder and decoder pass all ITU-T test vectors.

Resource Requirements^{*†‡}

Software	MCPS (Peak)	MCPS (Average)
Encoder (6.3k)	5.22	4.60
Encoder (5.3k)	5.90	5.37
Decoder	0.53	0.50

Software	Program Memory		Const Data Memory	Scratch Memory	Stack Memory	Per Channel Data Memory	
Encoder (6.3k)	9,888	+22,336	+9,856=42,080	18,932	4,020	280	1,418
Encoder (5.3k)			+9,024=41,248		4,868	312	1,418
Decoder	+8,512=18,400	2,432	240		402		

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* All MCPS results are based on TI CCS2.20.18 C62xx Cycle Accurate Simulator when encoding/decoding the ITU-T test vectors. The real cycles may vary when running on different C62x DSPs and different inputs.

† All memory usage figures are given in unit of byte.

‡ YYZC keeps the right to update the software without updating these figures.