

# G.722-C64x Data Sheet

## Introduction

Beijing YiYuanZhuCheng (YYZC) G.722 audio coding software is an implementation of ITU Recommendation G.722 64 kbit/s SB-ADPCM audio (50 to 7 000 Hz) coding system. The software runs on the Texas Instruments (TI) TMS320C64X family of Digital Signal Processors. The G.722 voice codec may be used for a variety of higher quality speech applications, which has three modes with bit rates: 64, 56 and 48 kbit/s. The latter two modes allow an auxiliary data channel of 8 and 16 kbit/s respectively. Both encoder and decoder pass all ITU-T test vectors.

## Resource Requirements<sup>\*†‡</sup>

Software	MCPS (Peak & Average)
Encoder	1.21
Decoder	1.01

Software	Program Memory	Const Data Memory	Stack Memory	Per Channel Data Memory
Encoder	1920	1098	32	132
Decoder	1600	1098	32	132
Encoder + Decoder	2848	1098	32	264

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## Contact

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\* All MCPS results are based on TI CCS2.20.18 C64xx Cycle Accurate Simulator when encoding/decoding the ITU-T test vectors. The real cycles may vary when running on different C64x DSPs and different inputs.

† All memory usage figures are given in unit of byte.

‡ YYZC keeps the right to update the software without updating these figures.